

L Number	Hits	Search Text	DB	Time stamp
-	1	(VHDL or verilog)and verif\$5 and 5553002.pn.	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/31 22:40
-	7	verilog same (coverage adj analysis)	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/19 16:22
-	24	verilog same flag	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 14:53
-	0	verilog same assingment	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 14:54
-	0	("verilog same assignment").PN.	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 14:55
-	0	("verilog same assignment").PN.	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 14:55
-	44	verilog same assignment	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 17:23
-	391	chowdhury.in.	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 17:27
-	1	chowdhury.in. and debashis	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 17:24
-	0	dasgupta-pallab.in.	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 17:25
-	402	dasgupta.in.	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 17:25
-	0	dasgupta.in. and pallab	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 17:26
-	20	dasgupta.in. and kumar	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 17:26
-	1	chowdhury-debashis.in.	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/21 17:28

	0	dudani-surrendra.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT	2004/03/21 17:33
-	15	(simulat\$ adj execution) same (HDL or (hardware near design near language) )	USPAT;	2004/03/22 15:20
-	10	(hardware adj description adj language) and label and simulat\$4 and (test adj bench)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT	2004/03/26 10:08
-	5	(hardware adj description adj language) and label and simulat\$4 and (test adj bench)	USPAT; EPO; JPO; DERWENT; IBM TDB USPAT	2004/03/26 10:26
-	245	(hardware adj description adj language) and ( partitioning)	USPAT; EPO; JPO; DERWENT; IBM TDB USPAT	2004/03/26 10:27
-	46	((hardware adj description adj language) and ( partitioning)) and monitoring	USPAT; EPO; JPO; DERWENT; IBM TDB USPAT	2004/03/26 10:27
-	9	((hardware adj description adj language) and ( partitioning)) and monitoring) and verilog	USPAT; EPO; JPO; DERWENT; IBM TDB USPAT	2004/03/26 10:34
-	1	((hardware adj description adj language) and ( partitioning)) and (observation adj point)	USPAT; EPO; JPO; DERWENT; IBM TDB USPAT	2004/03/26 14:29
-	227	VHDL and tag	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT	2004/03/30 09:30
-	90	(VHDL and tag) and verif\$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT	2004/03/30 09:31
-	48	((VHDL and tag) and verif\$6) and statement	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT	2004/03/30 09:31
-	48	((VHDL and tag) and verif\$6) and statement	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT	2004/03/30 09:41
-	832	vhdl and propagat\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT	2004/03/30 09:41
-	75	(vhdl and propagat\$5) and tag	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT	2004/03/30 09:41
-	10	((vhdl and propagat\$5) and tag) and (test adj cover\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT	2004/03/30 11:16
-	570	703/13.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/03/30 11:17

-	186	703/13.ccls. and verif\$6	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/03/30 11:19
-	25	(703/13.ccls. and verif\$6) and coverage	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/03/30 11:20
-	25	(703/13.ccls. and verif\$6) and coverage	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/03/30 11:22
-	27	703/13.ccls. and tag	USPAT; EPO; JPO; DERWENT; IBM TDB	2004/03/30 11:22
-	158	(VHDL or verilog)and verif\$5 and history	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/03/31 07:13
-	1	(VHDL or verilog)and verif\$5 and 6141630.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/03/31 22:39